

FIGURE 2



START

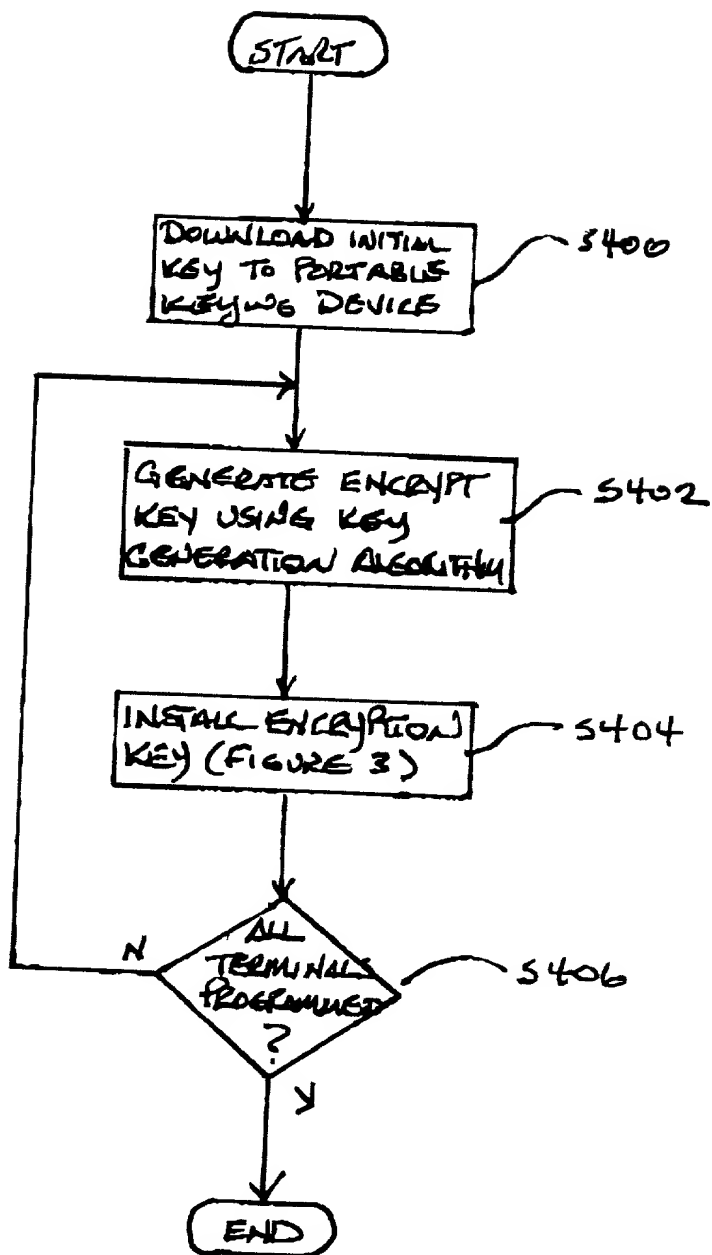


FIGURE 4

200

220

240

250

252

254

260

262

264

266

Microcomputer (204)

Memory (214)

Receiver/Transmitter (218)

Address

Operating Voltage

Programming voltage

Reg.

Reg.

For memory devices that do not generate their own programming voltage internally

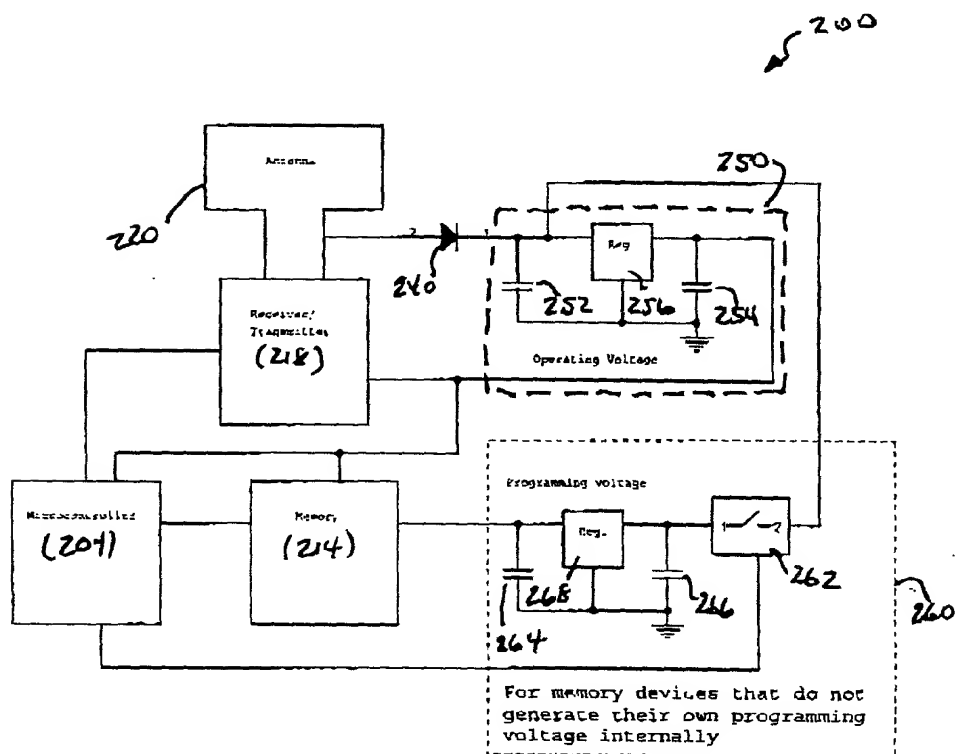


FIG. 6

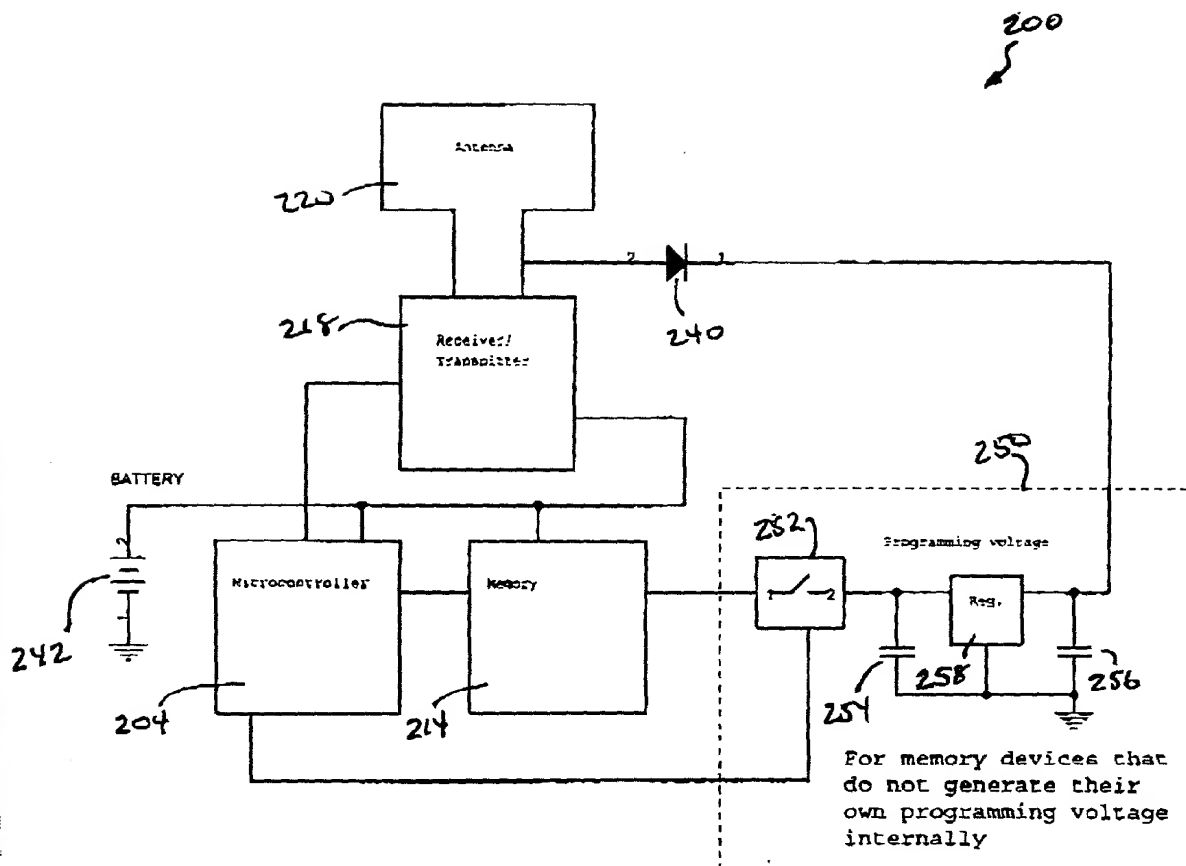


FIGURE 6

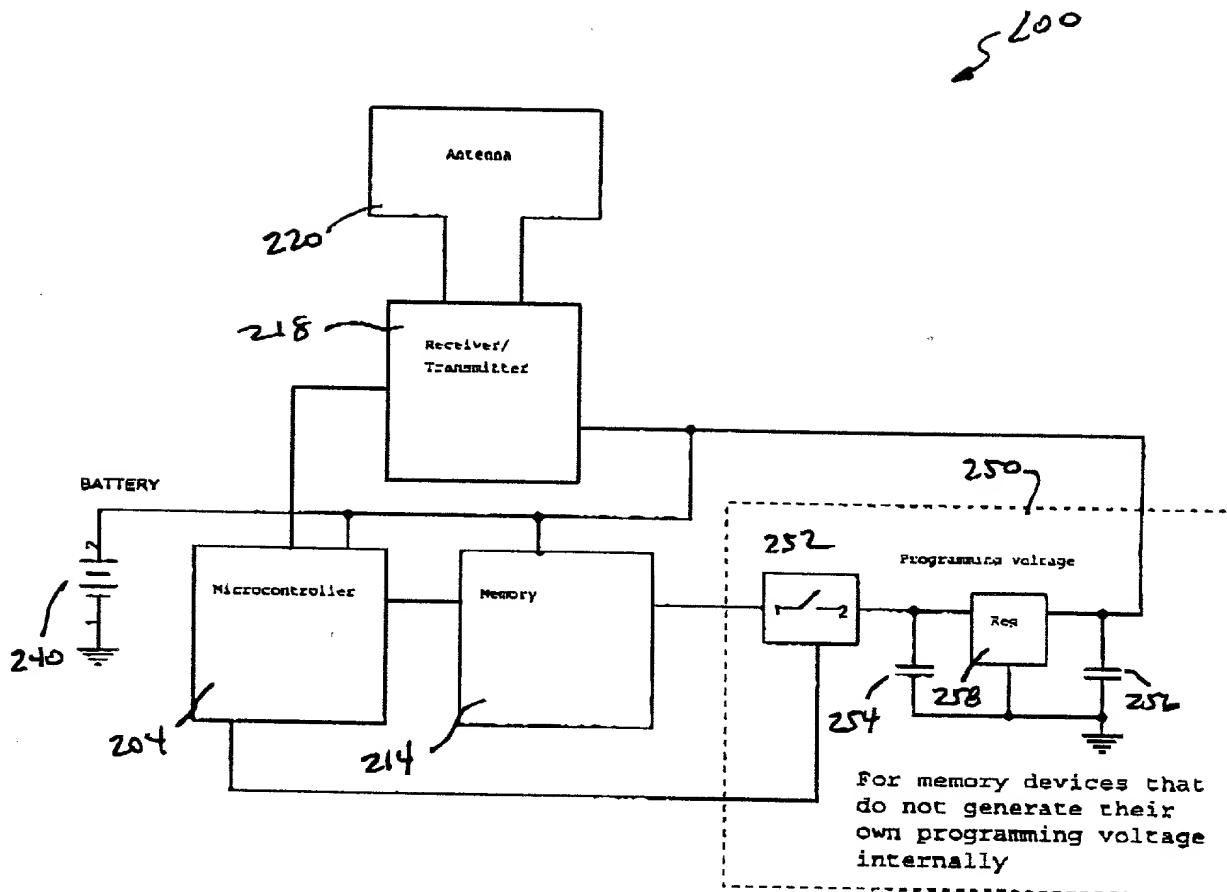


FIGURE 7